Title: INTELLIGENT RADIO DESIGN FOR SATELLITE GATEWAYS AND TERRESTRIAL BASE STATIONS Inventor: P. Chakravarhi et al Filed: July 16, 2001 Page | of 5

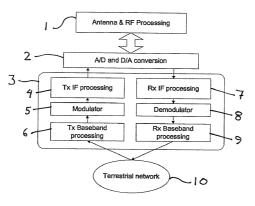


FIGURE 1

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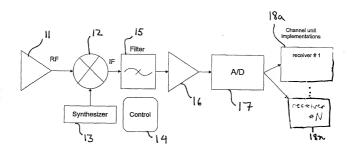
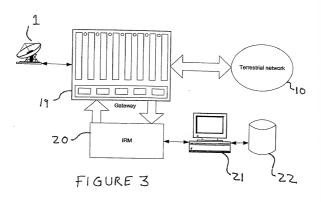


FIGURE 2

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AND TERRESTRIAL BASE STATIONS Inventor: P. Chakravarthi et al Filed: July 16, 2001 Page4 of 5 26 Pool of Algorithms SCU (1) Local Memory , Z7m SCU (N) Local Memory Controller HPCU (1) Local Memory IRM Local Memory HPCU (M) Local Memory

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FIGURE 4

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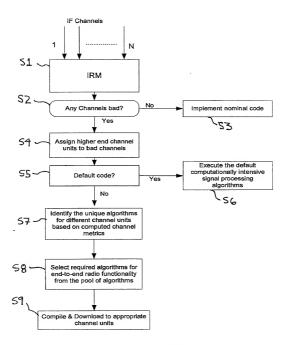


FIGURE 5